

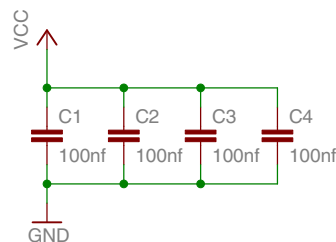
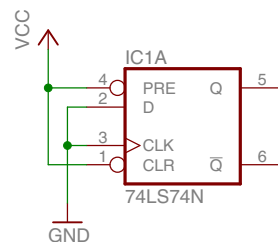
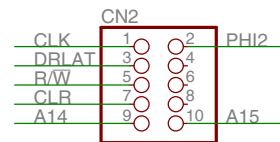
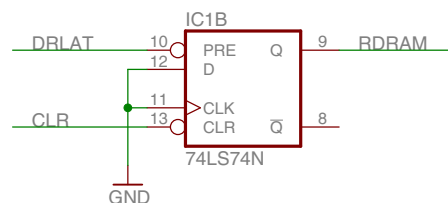
Modifications REQUIRED to Plus 3 board
Disconnect pins 3 and 13 of IC9 from board.
Connect pins 3 and 13 of IC9 to ground

Note if using 128K, 256K or 512K FLASH/EPROM, then Code must be in the *TOP* 64K of the ROM

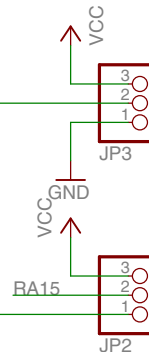
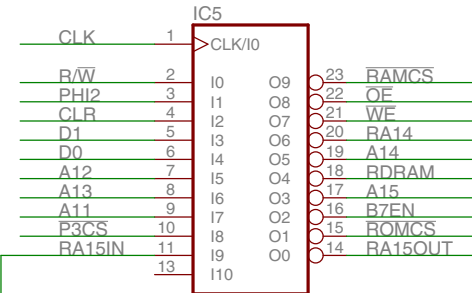
If using 32K or 64K ROM then plug into bottom 28 pins
Leave pins 1,2,31,32 unconnected

CN2 connections

CLK to IC10 pin 6 PHI2 to IC10 pin 5
DRLAT to IC10 pin 8
R/W to IC8 pin 9
CLR to IC12 pin 1 or IC15 pin 1 (RESET)
A14 to IC7 pin 5 A15 to IC7 pin 4



JP1: For 27256, 2-4, for 27512, 4-6
JP2 1-3 for bank 7 RAM, 3-5 for bank 7 ROM



TITLE: plus3_ramrom

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