

A BBC EPROM Programmer

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1 Introduction

This document describes an EPROM programmer I designed and built circa 1985, for use with a BBC microcomputer.

2 Using the programmer

The EPROM programmer should be plugged into the BBC computer's user port. The switch should always be set to the read position before starting the software. The software should be run from disc; this will show a menu allowing ROM images to be loaded, saved, programmed, and verified, and operating system commands to be issued.

3 Hardware

I "designed" the EPROM programmer by adapting an existing design, published in the Beebug magazine. My alterations were to add a second address latch and a state machine to select the read/write functions and latches. This allowed the programmer to be driven with just the user port, instead of using the printer port as well (as the Beebug design had done). My knowledge of hardware was just adequate for the task; the board works fine, but more experienced hardware designers will probably find flaws in the design.

The circuit diagram is shown in figure 1. Note that the 27128 EPROM is shown with the pins mirrored left for right, because the socket was mounted on the reverse (track) side of the board I built the programmer on. Also, for cleanliness in the layout, I have shown the +5v and 0v pins at the wrong ends of the edge connector; be careful when laying out a board not to copy this order onto the board.

The circuitry at the bottom selects the programming functions. The CB2 line from the user VIA (6522 interface adapter) is used to control the programmer, with the CB1 line providing feedback. The low and high address latches are loaded first, and then the output enable or program enable are driven low to read or write the EPROM. Table 2 shows the state machine implemented by this hardware. The outputs from the circuit are CB1 (used for feedback to the BBC so it can detect which state the programmer is in), Enable (set low to make the EPROM read or write data from or to the data bus), CK_{LO} (used to set the low order address latch) and CK_{HI} (used to set the high order address latch). The CB2 line controls the state machine; it drives the clock line of the 74LS74 flip-flop, triggering a new state when it is set low and then high again. The 74LS374 latches are also edge-triggered, so the latches will only capture data from the data bus when transitioning from low to high. There are only three states in

BBC EPROM Programmer circuitry, Angus Duggan, 1985

Note: 27128 is shown with pins mirrored left-right, because socket is mounted on reverse (track) side of board.

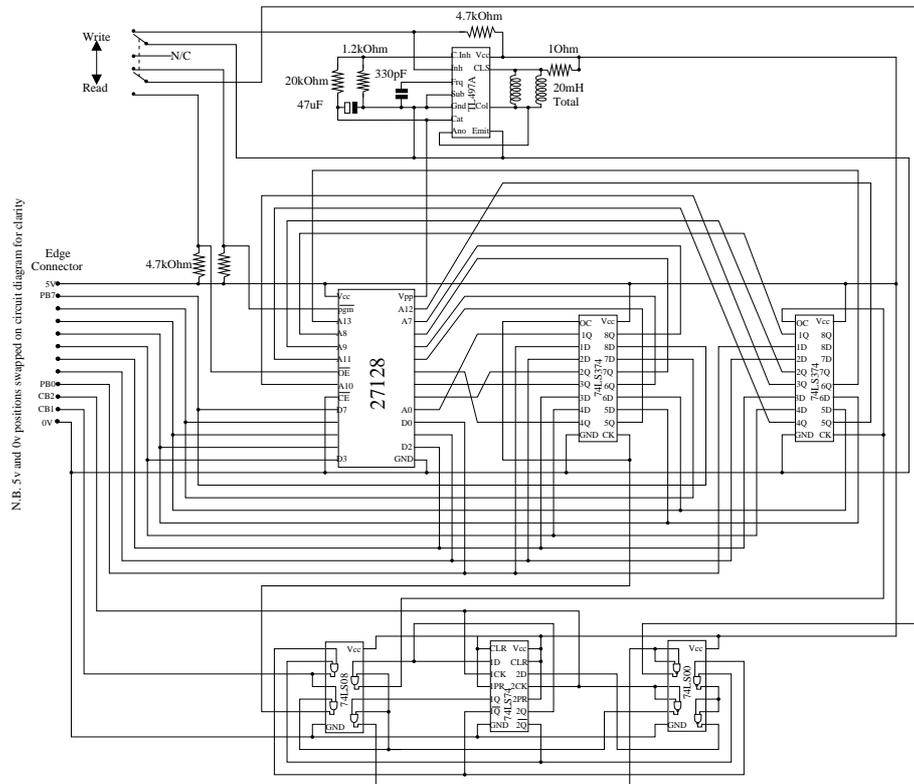


Figure 1: Circuit diagram of EPROM programmer

the cycle; the remaining possible state (when Q_1 and Q_2 are both 1) will transition to the R/W state, and then the state cycle will repeat. When the software initialises the programmer, it repeatedly clocks the state until the CB1 line changes.

I implemented the design using vero-board, managing to fit it into a 37 by 36 section of 0.1 inch pitch board. Photos may be found on my web site at <http://knackered.org/~angus/beeb/>.

4 Software

The software to drive the EPROM programmer is a 6502 assembly program. It performs the functions of loading ROM images, saving ROM images, reading, writing, and verifying EPROMs. The 6502 assembler source is shown below, in the format for my own assembler. Some assembler directives (EQB, EQA, EQD) may be unfamiliar. Their meanings are:


```

        ora pcr
        sta pcr
        clc
        rts

VERIFY    dex
          bpl CHECK
\ Verify eprom against buffer
          jsr READY
          bcs KILPROG
          jsr PRINT
          eqw P28-P27
P27       eqb 31, width/2-9, 4
          ega "Verifying"
P28       ldx #<LOOPVIFY
          ldy #>LOOPVIFY
          jsr LOOP
          jmp DONE
KILVIFY   jmp MAIN

LOOPVIFY  jsr READ
          cmp (baddr), Y
          clc
          beq R4
          sec
          rts
R4

CHECK     dex
          bpl MOSCALL
\ Check blank eprom
          jsr READY
          bcs KILPROG
          jsr PRINT
          eqw P30-P29
          eqb 31, width/2-8, 4
          ega "Checking"
P29       ldx #<LOOPCHK
          ldy #>LOOPCHK
          jsr LOOP
          jmp DONE
          jmp MAIN
KILCHK   jmp MAIN

LOOPCHK   jsr READ
          cmp #&FF
          clc
          beq R6
          sec
          rts
R6

MOSCALL   \ Operating system call
          jsr COMMIND    \ set up command window
          lda #' '
          jsr oswrch     \ indicate input required
          lda #0        \ OS input line
          ldx #<OSLINE
          ldy #>OSLINE
          jsr osword    \ read a line from input
          bcs KILLOSC   \ input error
          lda #14
          jsr oswrch    \ page mode on
          ldx #<INPUT
          ldy #>INPUT
          jsr osccli
          lda #15
          jsr oswrch
          jmp MAIN
KILLOSC  jmp MAIN

LOOP      stx middle    \ Common loop for prog etc.
          sty middle+1
          jsr PRINT
          eqw P32-P31
          ega "...& "
          ldy #0
          jsr ESCAPE    \ check escape key
          bcs QUITLOOP
          lda #8        \ get to right place
          jsr oswrch
          jsr oswrch
          lda eaddr
          jsr HEX       \ print high address
          tya           \ print low address
          jsr HEX
          lda #8        \ move back into position
          jsr oswrch
          jsr oswrch
          lda #&FF
          sta ddrb
          sty orb       \ load low address
          jsr TOGGLE
          lda eaddr     \ load high address
          sta orb

          jsr TOGGLE
          ldx CBI
          jsr MIDDLE    \ do centre routine
          bcs NOTEQUAL
          cpx CBI       \ check CBI interrupt
          bne PROGOK
          jmp PROGERROR
          jsr PROGOK
          jmp PROGOK    \ increment address
          iny
          bne REPEAT
          inc eaddr
          inc baddr+1
          dec length
          bne RECHECK
          clc
          rts
          jsr PRINT
          eqw P34-P33
          eqb 7, 31, width/2-8, 5
          ega "Comparison error"
          sec
          rts
          jmp (middle)  \ indirect
          bcs P37
          jsr PRINT
          eqw DONE2-P35
          eqb 31, width/2-2, 5
          jsr PRINT
          eqw P37-P36
          ega "Done"
          jmp MAIN
          pha           \ print two hex digits
          lsr A
          lsr A
          lsr A
          lsr A
          jsr DIGIT
          pla
          and #&F
          cmp #10
          bcc NUMBER
          adc #6
          adc #48
          jmp oswrch
          jsr COMMIND   \ get ready to READ
          jsr PRINT
          eqw P18-P17
          ega " Set the programmer switch to "
          eqb 130
          ega "READ,"
          jmp PREPARE
          lda #0        \ read byte from programmer
          sta ddrb
          lda #&DF
          and pcr
          sta pcr
          lda irb
          pha
          lda #&F0
          ora pcr
          sta pcr
          pla
          rts
          jsr COMMIND   \ get ready to WRITE
          jsr PRINT
          eqw P20-P19
          ega " Set the programmer switch to "
          eqb 129
          ega "WRITE,"
          jmp PREPARE
          jsr PRINT
          eqw P22-P21
          eqb 31, 1, 1
          ega "then select the EPROM type -"
          eqb 13, 10, 132, 31, menux-1, 2, 135
          ega "1 - 2764"
          eqb 13, 10, 132, 31, menux-1, 3, 135
          ega "2 - 27128"
          eqb 13, 10
          lda #&80
          sta eaddr
          lda #&40
          sta length
          lda #&3C
          sta baddr+1
          \ EPROM start at &8000
          \ default length = &4000
          \ Buffer start at &3C00

```

	lda #0				bne R2	
	sta baddr				inc zpwork+1	
GETLEN	jsr osrdch			R2	rts	
	bcs R5			BRKERR	ldx STACK	\ action taken on BRK
	cmp #'2'				txs	
	beq LENOK				jsr PRINT	
	cmp #'1'			P38	eqw P39-P38	
	bne GETLEN				eqb 15, 13, 10, 7	
	lsl length				ega *	OS Error : *
	lda #11			P39	ldy #1	
LENOK	jsr oswrch			BRKMSG	lda (brkmsg), Y	
	lda #11				beq BRKQUIT	
	jsr oswrch				jsr oswrch	
	lda #9				iny	
	jsr oswrch			BRKQUIT	bne BRKMSG	
	lda #157				jsr osnewl	
	jsr oswrch		\ make outputs safe		jmp MAIN	
	lda #&FF			IRQ	pha	\ interrupt routine
	sta ddrb				lda ifr	\ test interrupt condition
	sta orb				and #32	\ timeout ?
	ldx CBI		\ take old count		beq AGAIN	
	jsr TOGGLE				lda t21	\ clear interrupt condition
	cpx CBI		\ once	AGAIN	inc TIMER	
	bne QUITPREP				lda ifr	
	jsr TOGGLE				and #16	\ CBI ?
	cpx CBI		\ twice		beq CHAIN	
	bne QUITPREP				lda orb	\ clear interrupt condition
	jsr TOGGLE			CHAIN	inc CBI	
	cpx CBI		\ three times...		pla	
	beq PROGERROR				jmp (OLDIRQ)	\ goto next interrupt handler
QUITPREP	clc			COMMWIND	jsr PRINT	\ setup command window
R5	rts				eqw P6-P5	
PROGERROR	jsr PRINT		\ No response from programmer	P5	eqb 28, commx, 24, commx+width-1, commy, 12	
	eqw P24-P23			P6	rts	
P23	eqb 7, 31, width/2-12, 5, 136			FILEWIND	jsr PRINT	\ setup filename window
	ega "EPROM Programmer Error"				eqw P10-P9	
	eqb 13, 10			P9	eqb 28, 15, filey, 34, filey, 12	
P24	sec			P10	rts	
	rts					\ Data area follows...
TOGGLE	lda #&DF		\ make CB2 go low then high	OPTIONS	eqb '1', '2', '3', '4', '5', '6', '*'	
	and pcr			OSLINE	eqw INPUT	\ OSWORD 0 block for commands
	sta pcr		\ low		eqb 255, 32, 127	
	ora #&F0			FILINE	eqw INPUT	\ OSWORD 0 block for filenames
	sta pcr		\ high		eqb 19, 32, 127	
	rts			CBI	eqb 0	\ counter for CBI interrupts
ESCAPE	lda escape		\ test & reset escape condition	TIMER	eqb 0	\ counter for t2 timeouts
	clc			OLDIRQ	eqw 0	\ Old IRQV2
	bpl NOESC			STACK	eqb 0	\ stack pointer
	lda #126			OLDBRK	eqw 0	\ Old brkvec
	jsr osbyte			BLOCK	eqw INPUT	\ Load file parameter block
	sec		\ carry set if escape detected		eqd 0	
	rts				eqd 0	
NOESC	rts				eqd 0	
PRINT	pla		\ print in-line codes		eqd 0	
	sta zpwork			LOADINFO	eqd buffer	
	pla		\ two byte size		eqd 0	
	sta zpwork+1				eqd 0	
	jsr GETIND			SAVEINFO	eqd &FFFF8000	
	sta zpwork+2				eqd &FFFF8000	
	jsr GETIND				eqd buffer	
	sta zpwork+3				eqd buffer+4000	
	jsr GETIND			INPUT	\ input buffer	
	jsr oswrch					
	lda zpwork+2					
	bne DELOW					
	dec zpwork+3					
	dec zpwork+2					
	bne PRLOOP					
	lda zpwork+3					
	bne PRLOOP					
	jsr INCADR					
	jmp (zpwork)					
GETIND	ldy #1		\ get data from indirect address			
	lda (zpwork), Y					
INCADR	inc zpwork					